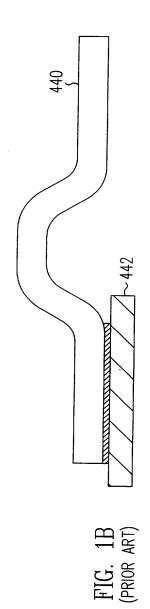
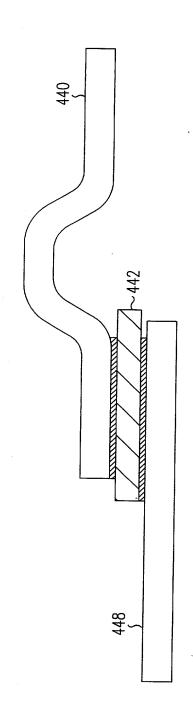


FIG. 1A (PRIOR ART)



 \sim 440 444 442 FIG. 1C (PRIOR ART)



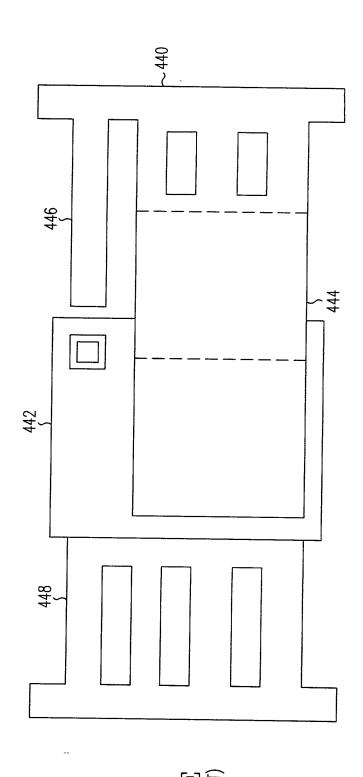
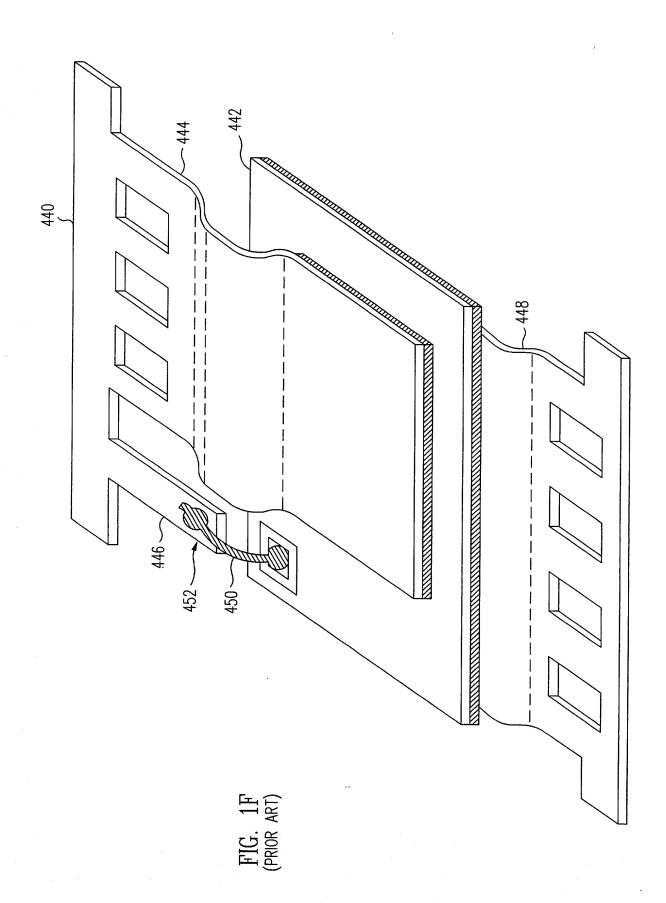
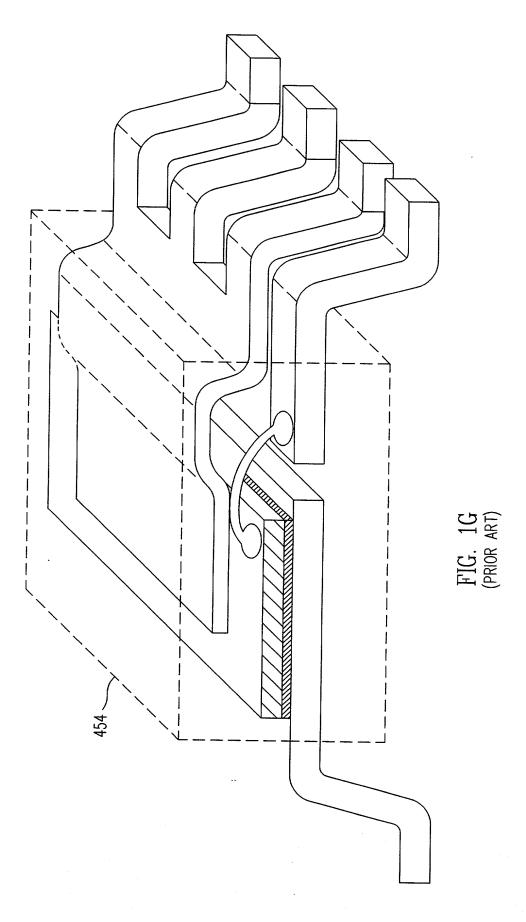
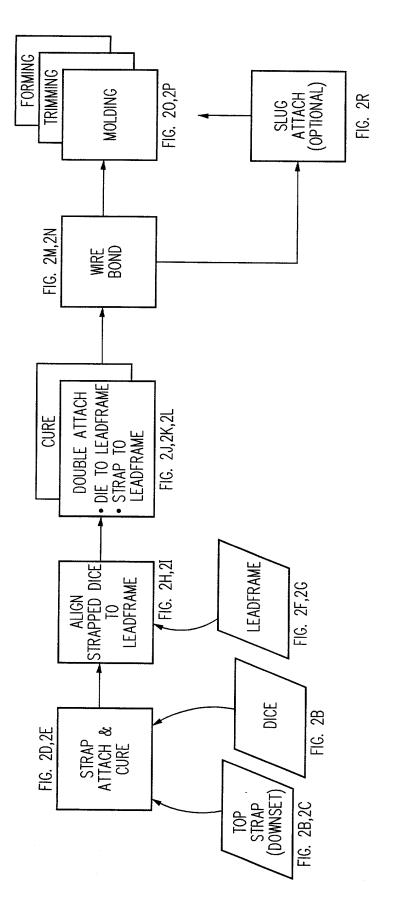


FIG. 1D (PRIOR ART)

FIG. 1E (PRIOR ART)



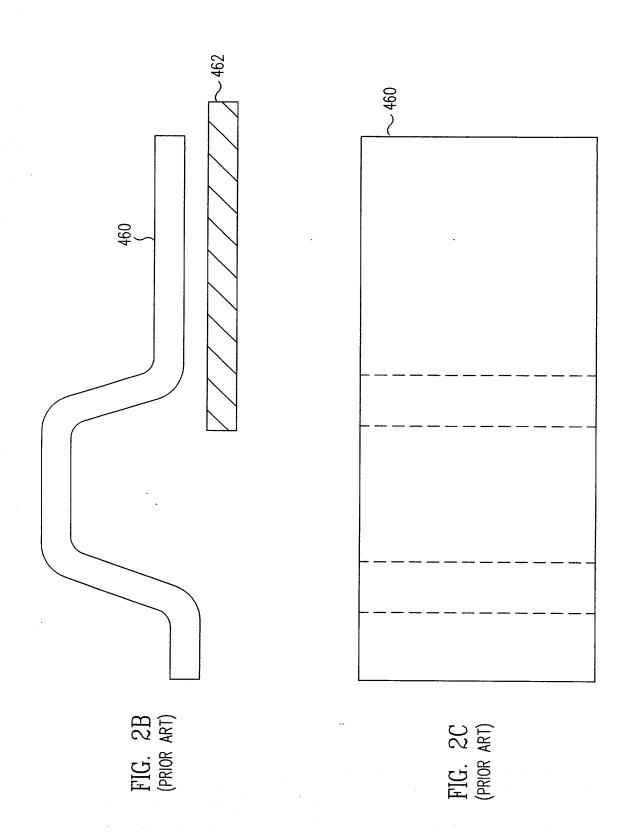


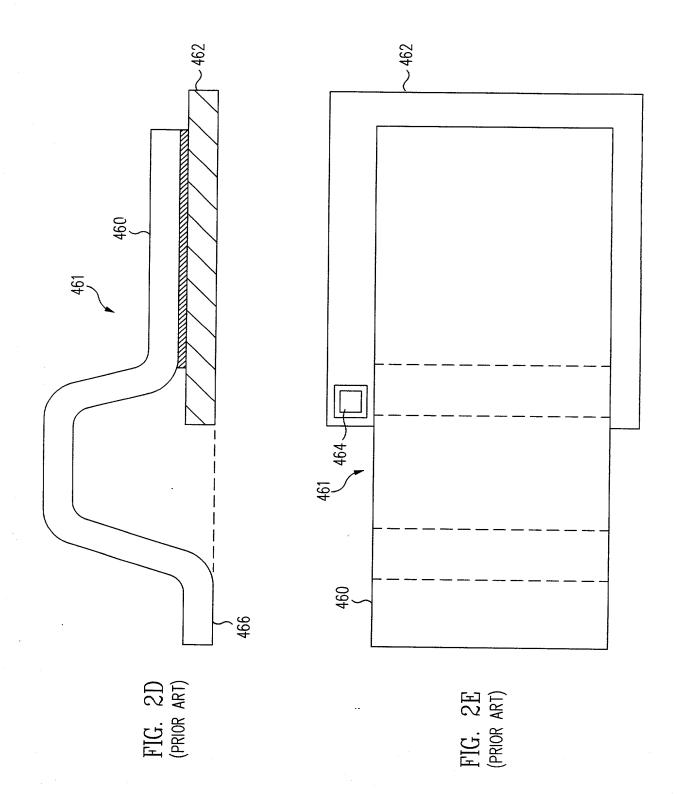


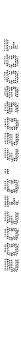
tions from grow spire transfer from the first transfer

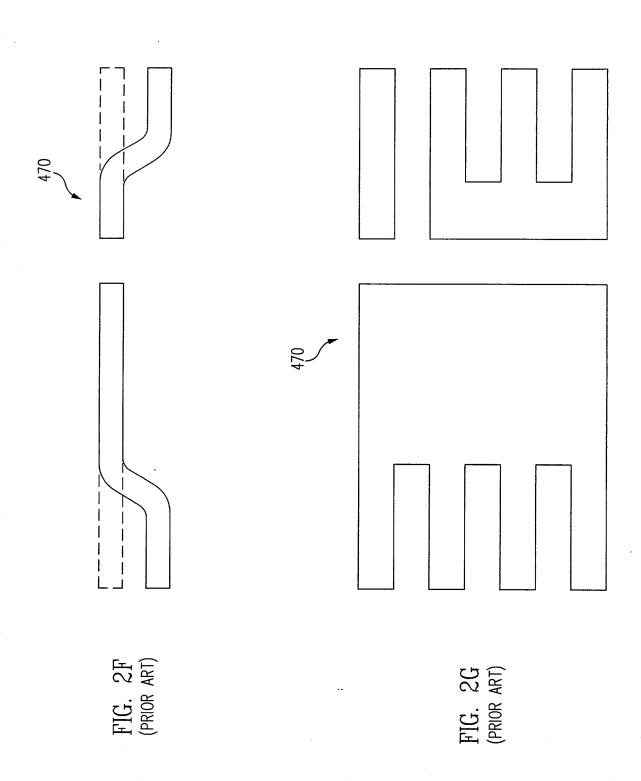
FIG. 2A (PRIOR ART)

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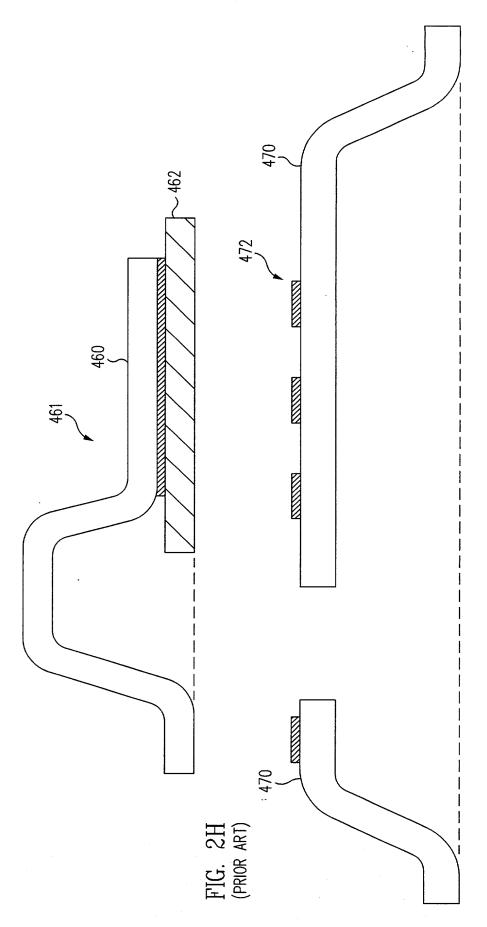




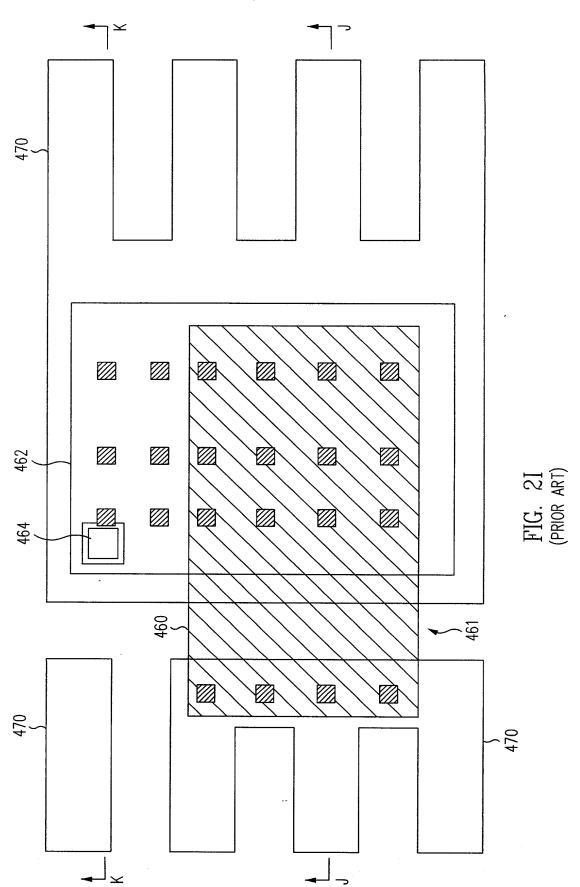


Symmetrical Package For Semiconductor Die Lam. Allen K. ET AL.

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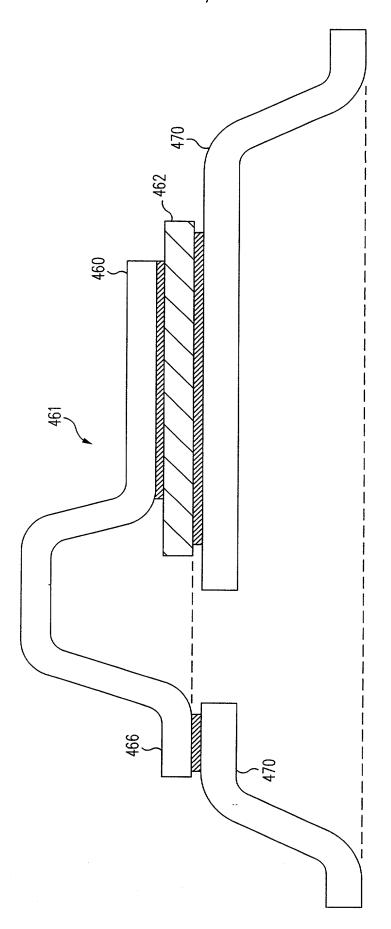


FIG. 2J (PRIOR ART)



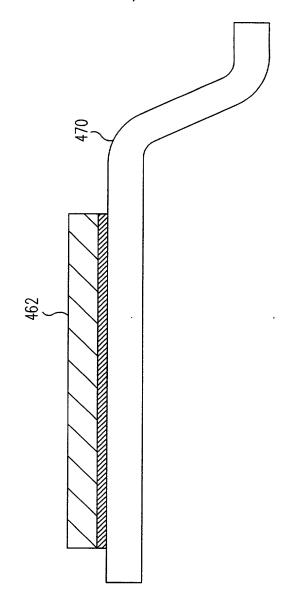
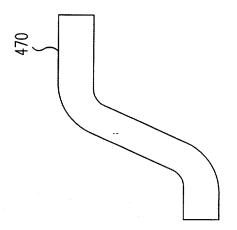


FIG. 2K (PRIOR ART)



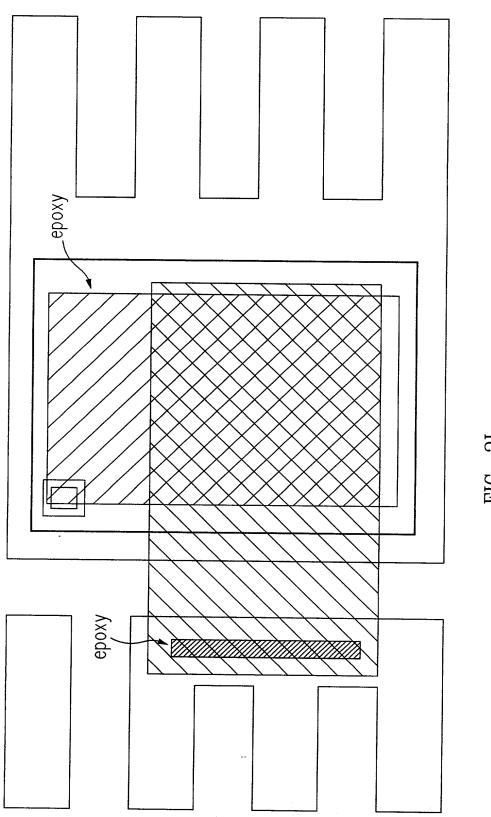


FIG. 2L (PRIOR ART)

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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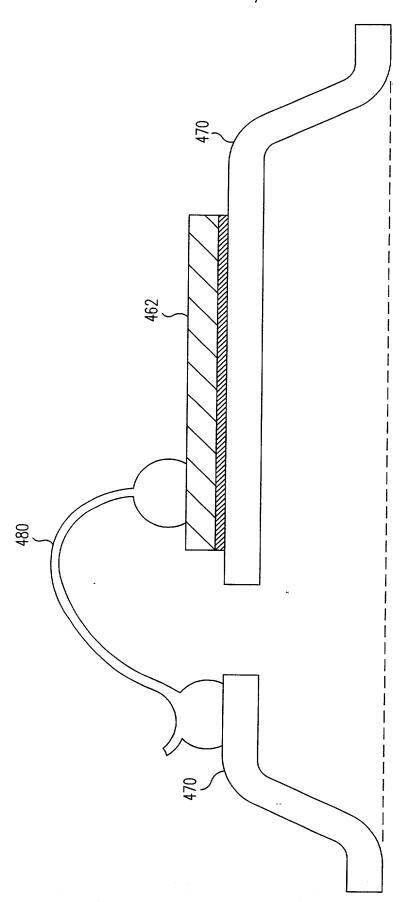
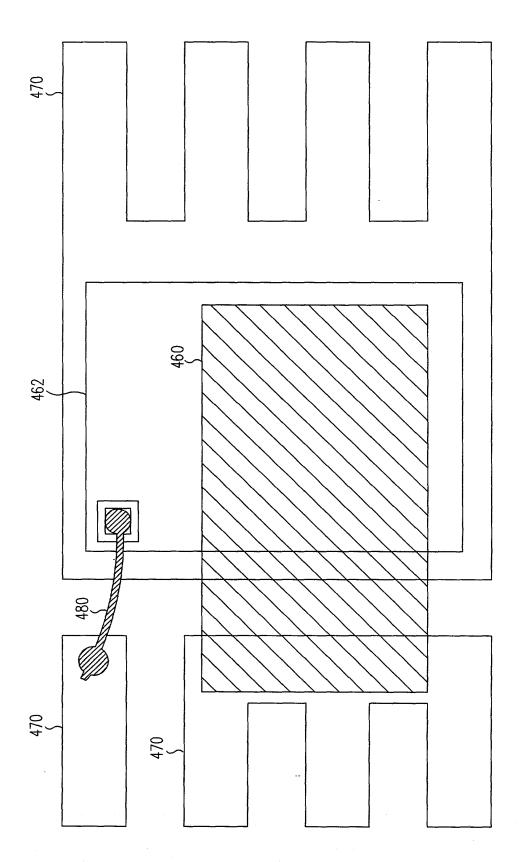


FIG. 2M (PRIOR ART)



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FIG. 2N (PRIOR ART)

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

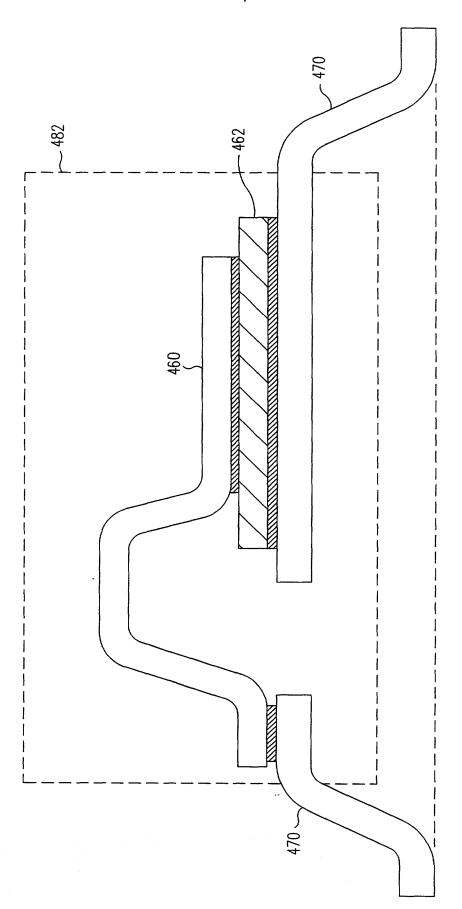
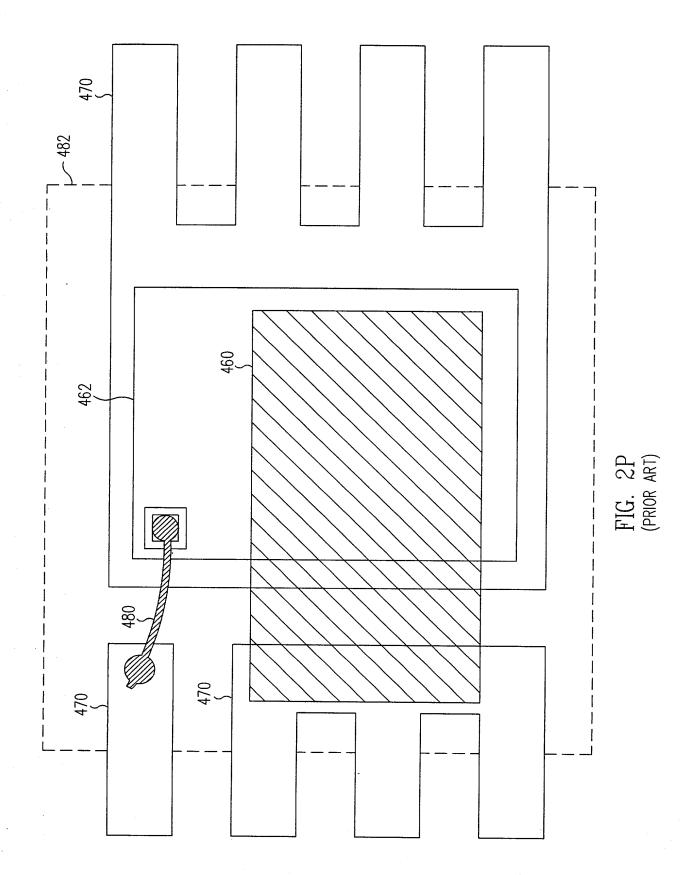
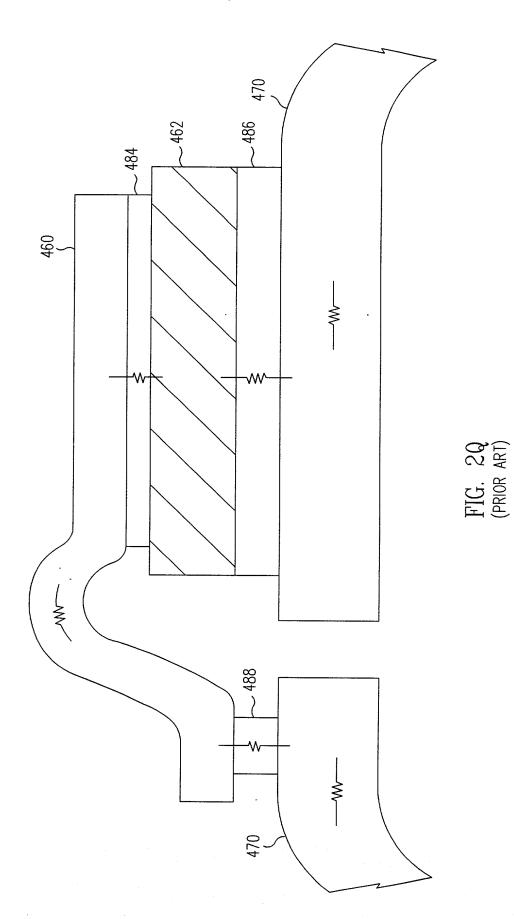
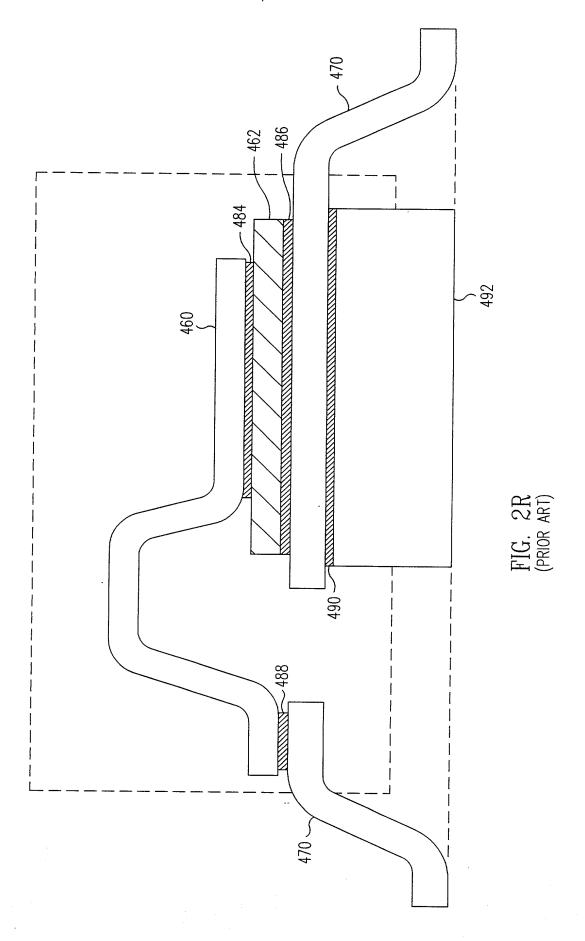


FIG. 20 (PRIOR ART)

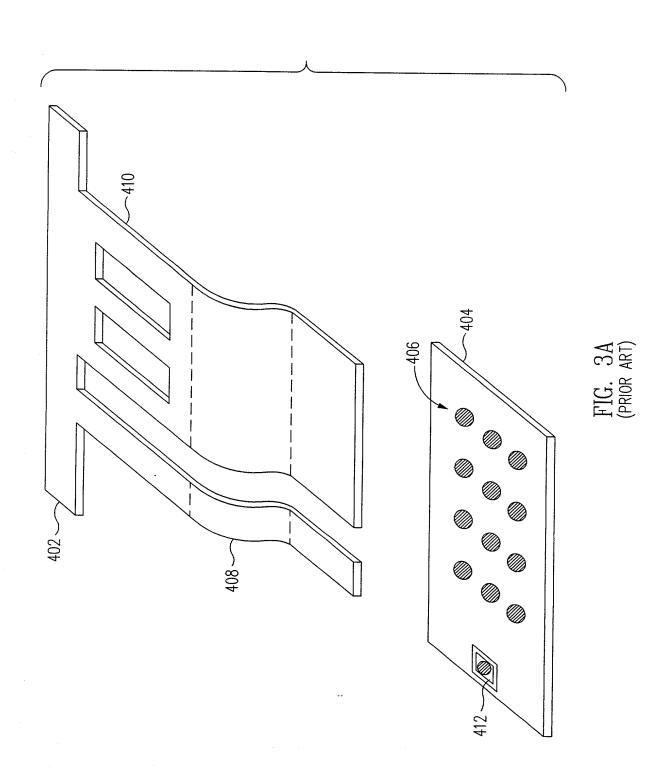






Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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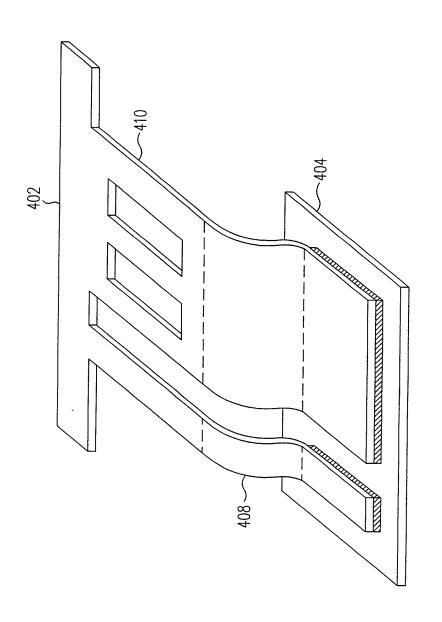


FIG. 3B (PRIOR ART)

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

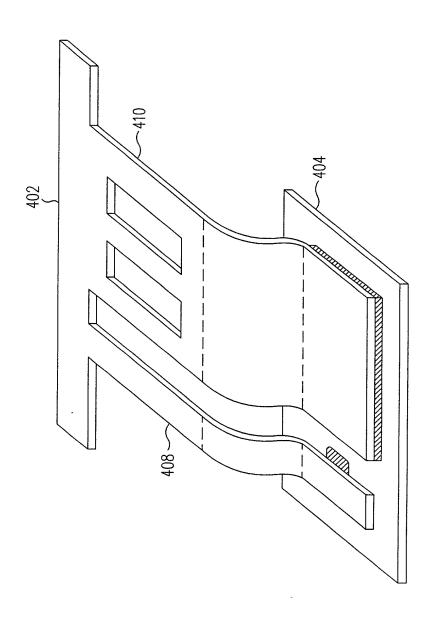
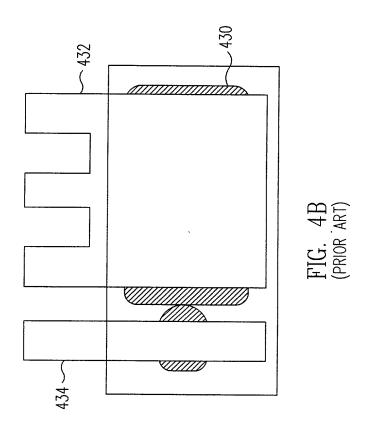
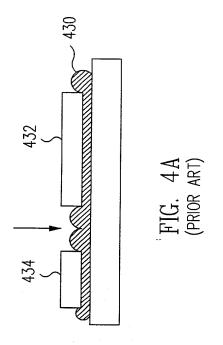


FIG. 30 (PRIOR ART

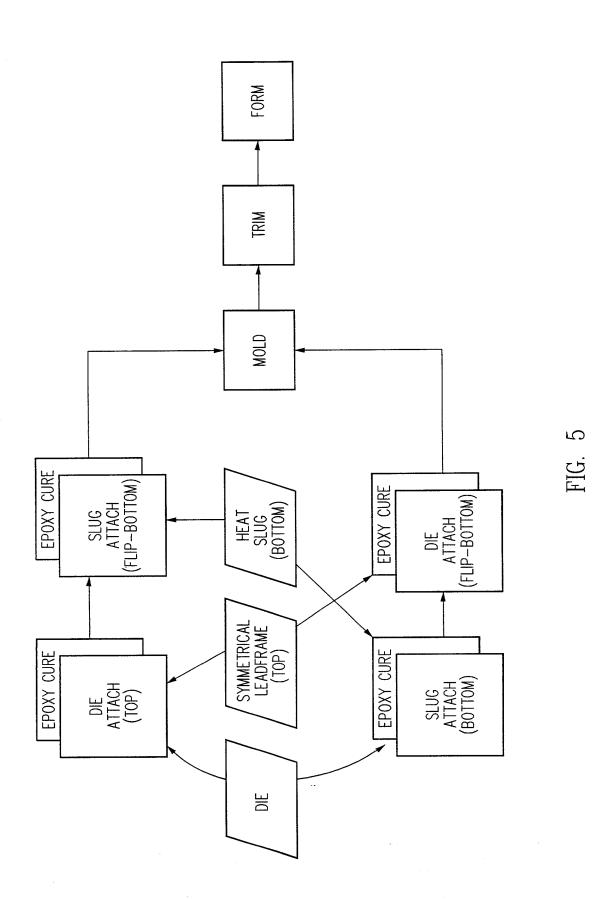
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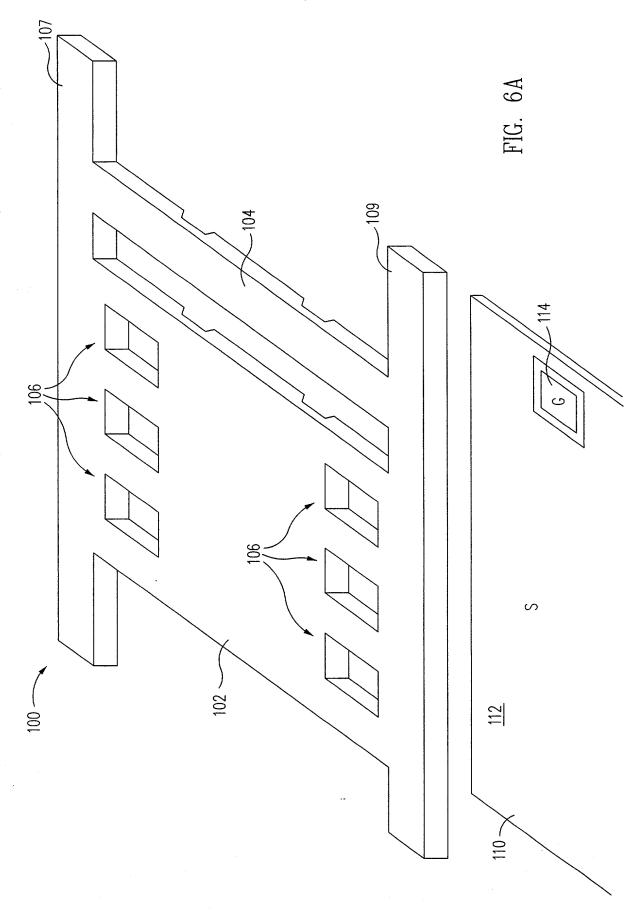
FIG. 3H (PRIOR ART) FIG. 3E (PRIOR ART) FIG. 3G (PRIOR ART) FIG. 3D (PRIOR ART) FIG. 3F (PRIOR ART)





Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 26/60





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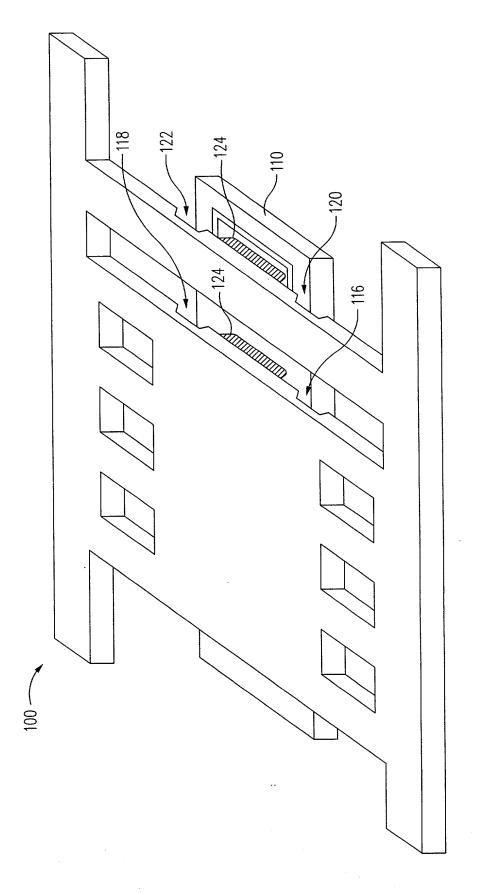
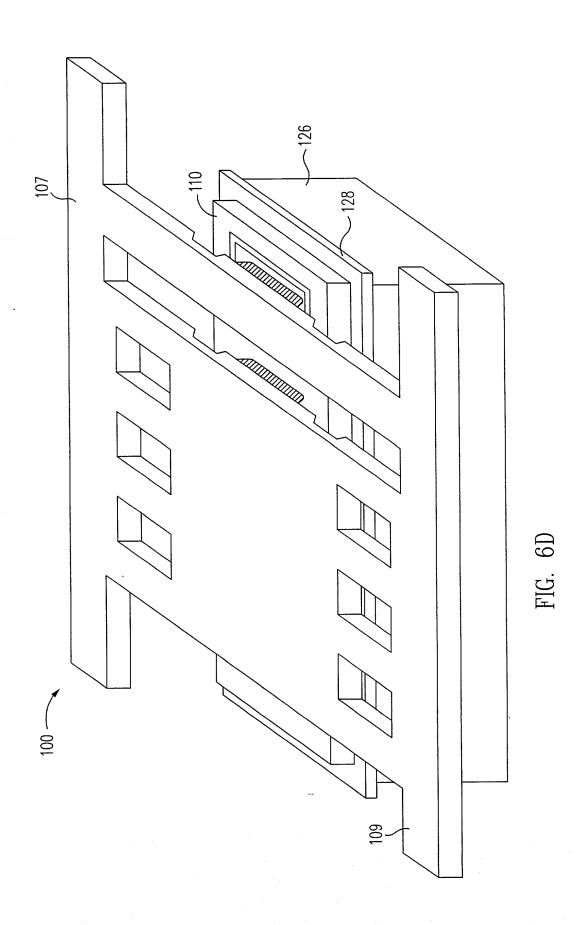
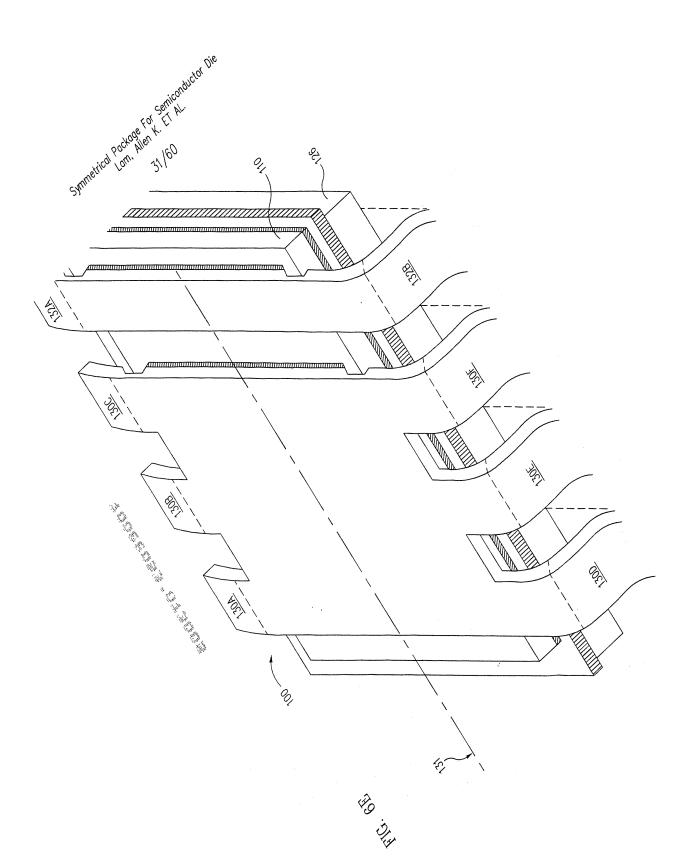


FIG. 6C

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

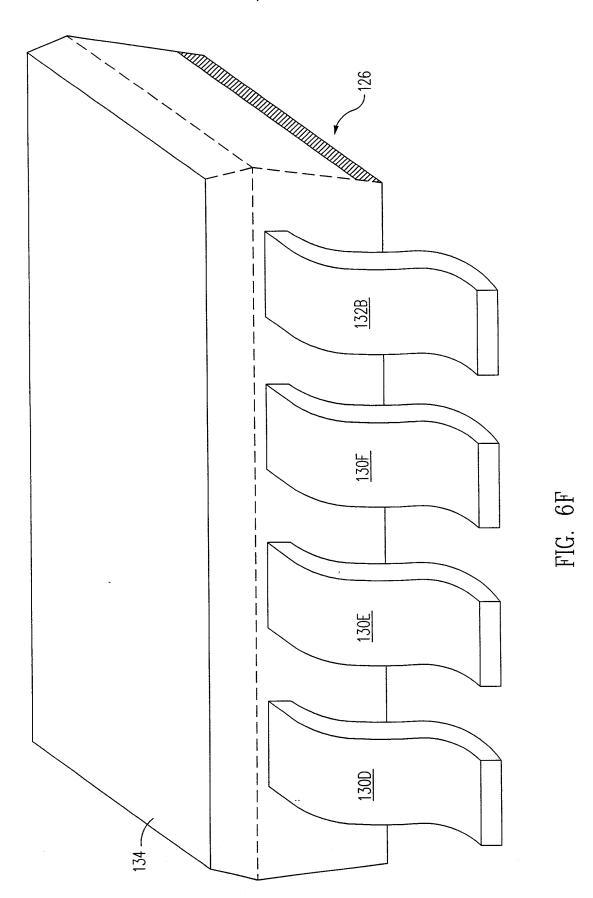
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Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 33/60

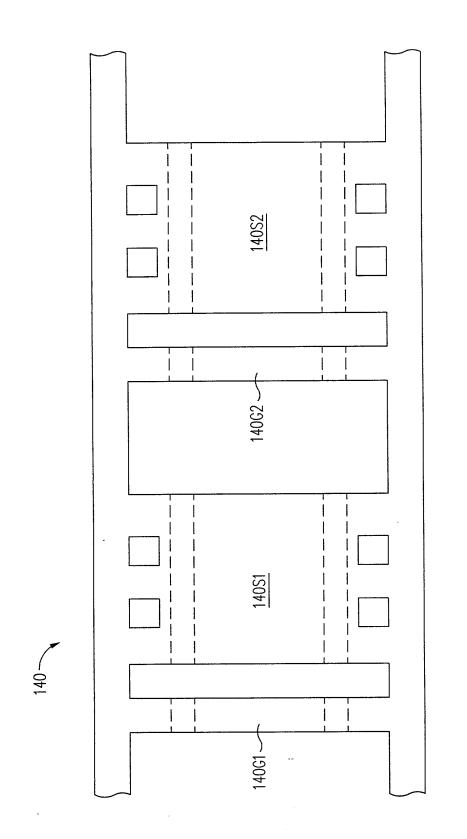


FIG. 7A

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 34/60

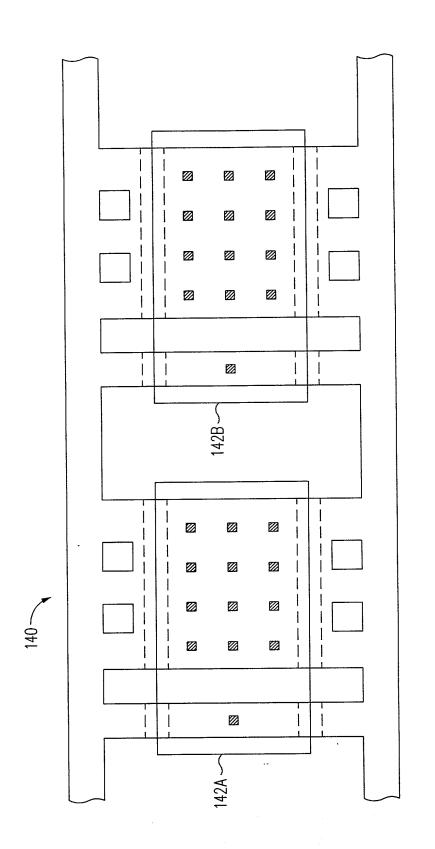


FIG. 71

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 35/60

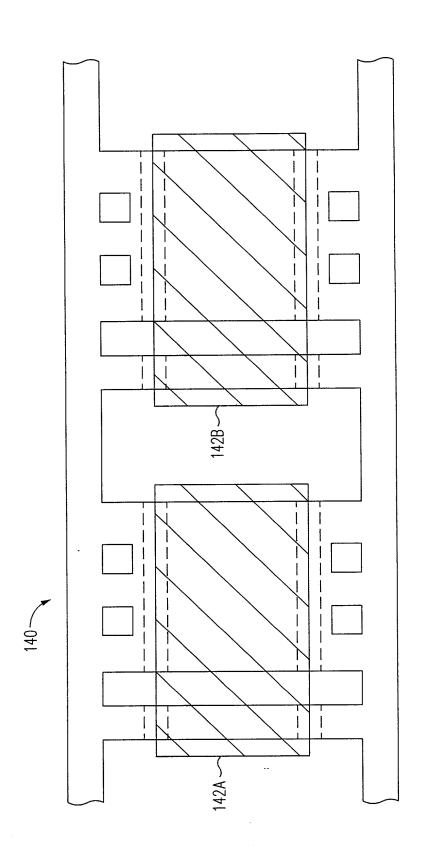


FIG. 7

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 36/60

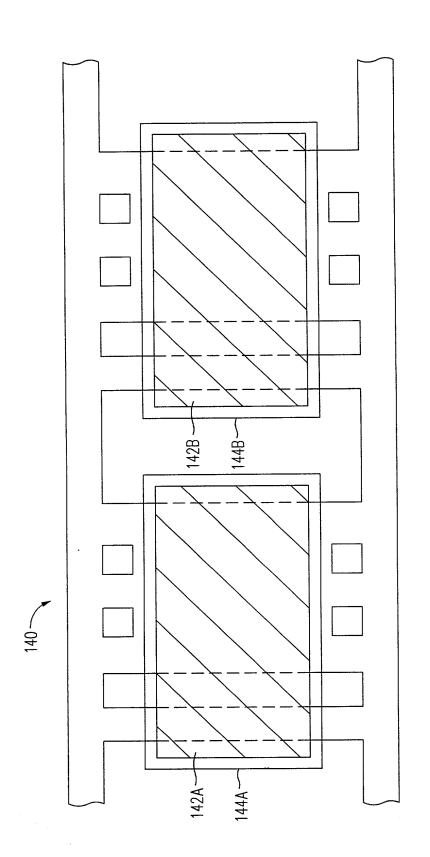


FIG. 71

Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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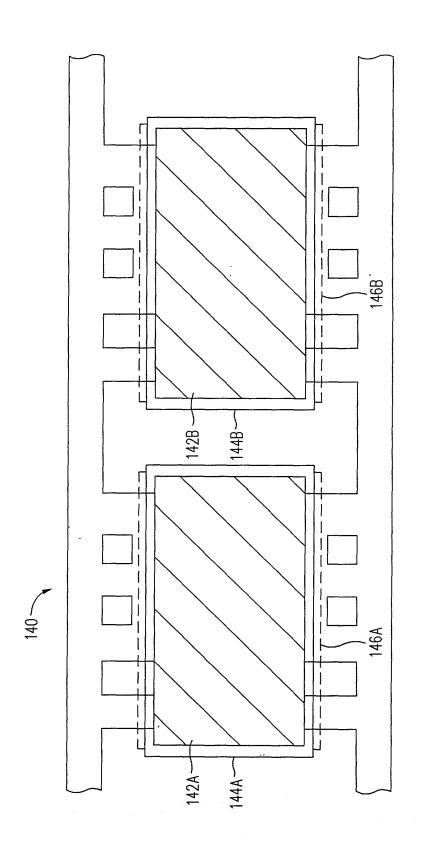
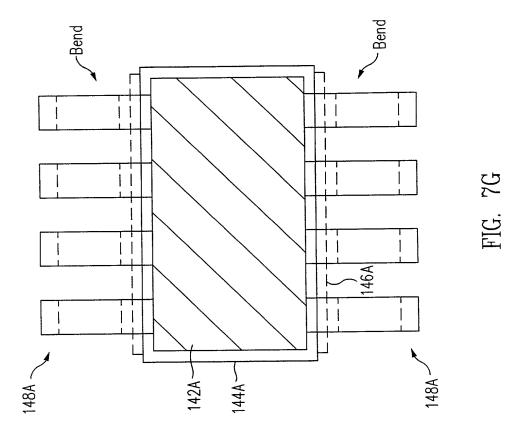
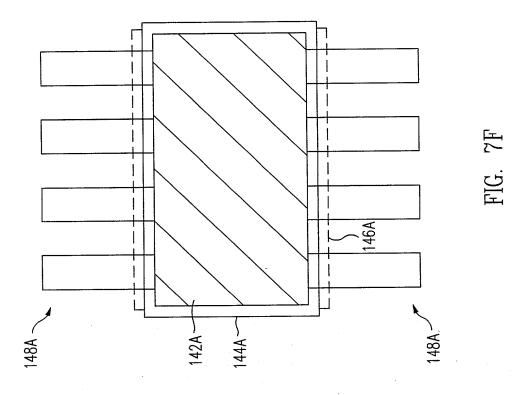


FIG. 71

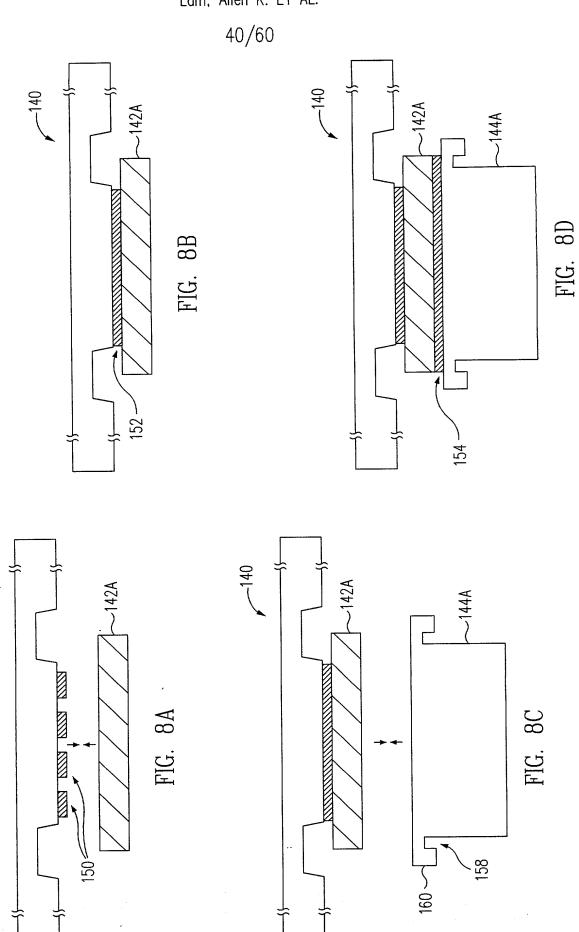
38/60



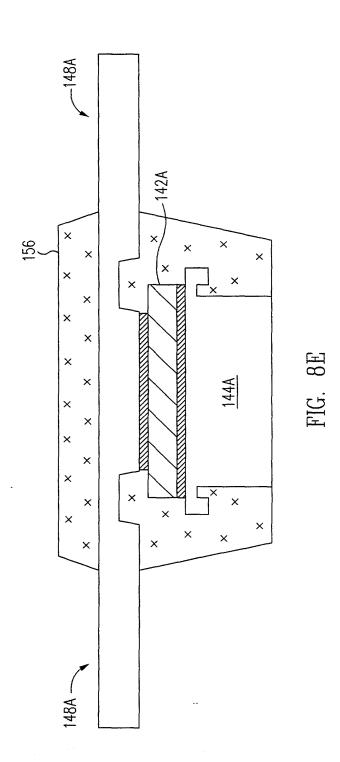


146B Plastic 149 146A

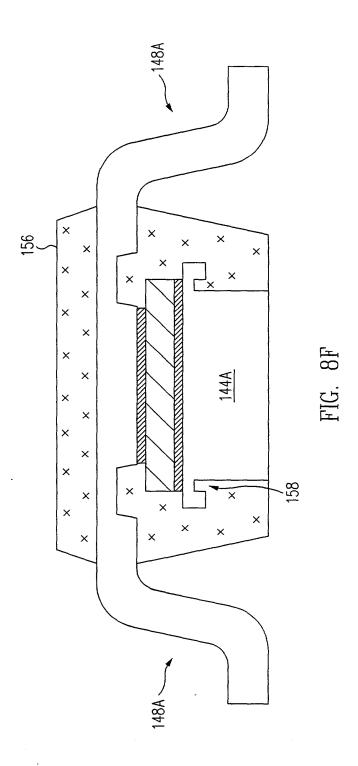
FIG. 7F





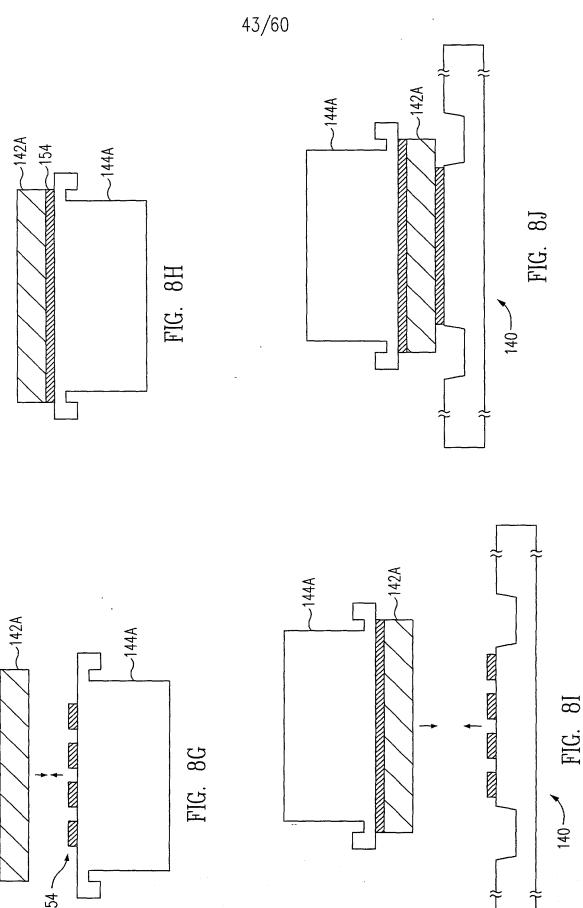


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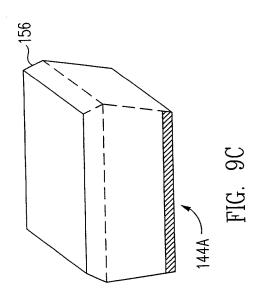


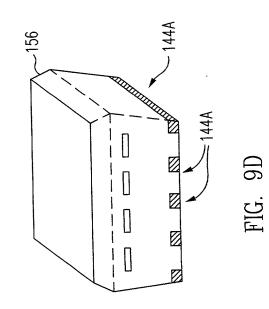
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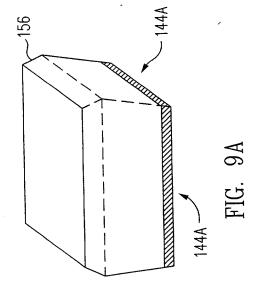
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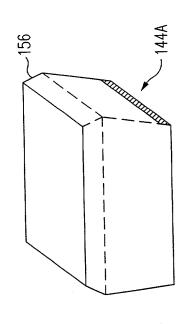
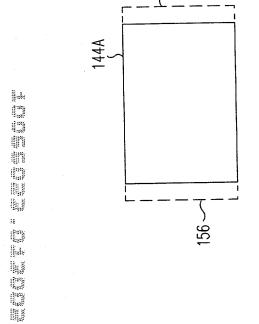
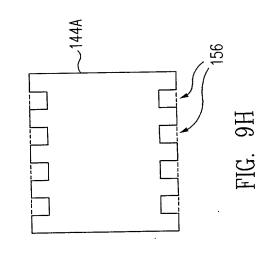
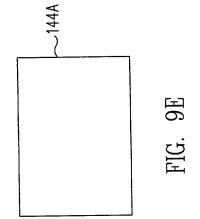


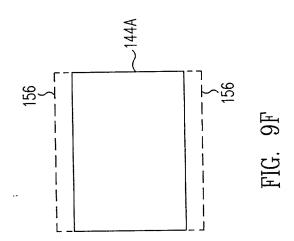
FIG.

FIG. 9G

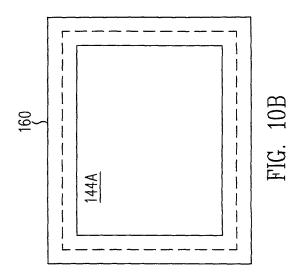


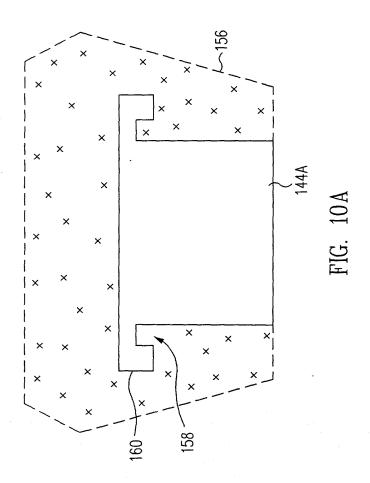




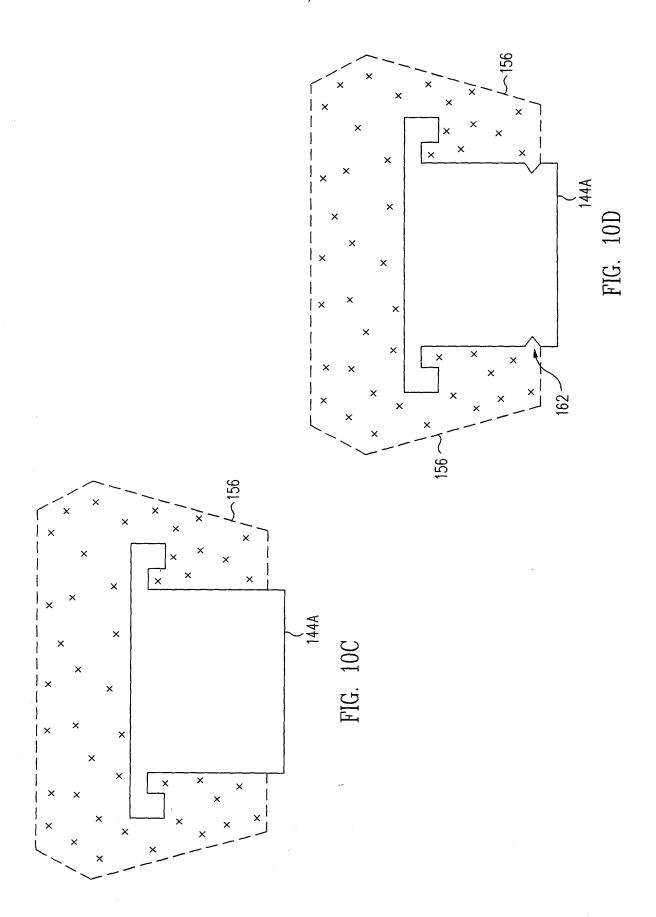


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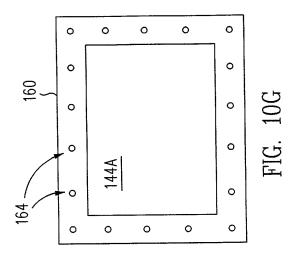


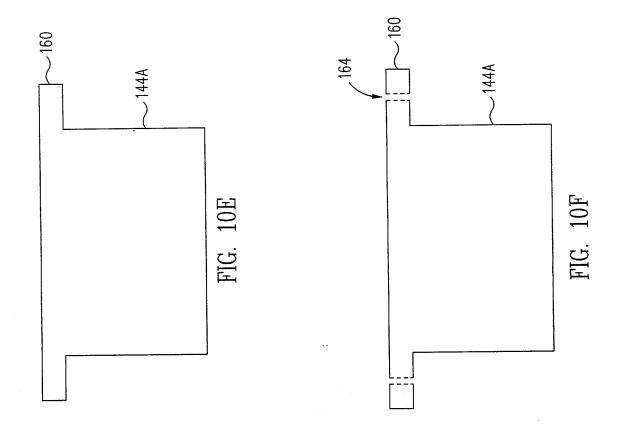


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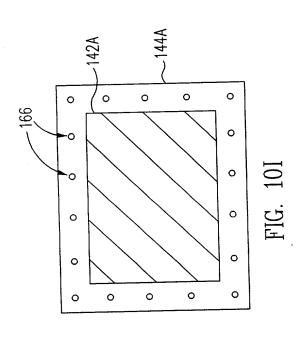


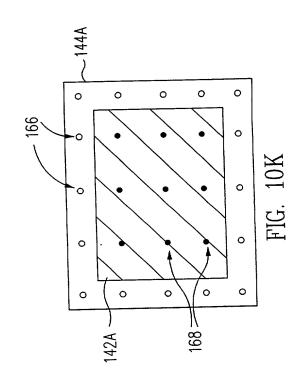
48/60

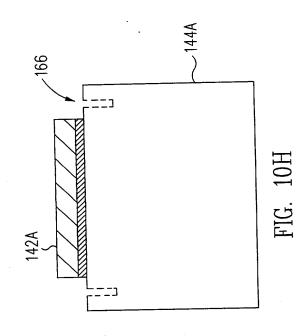


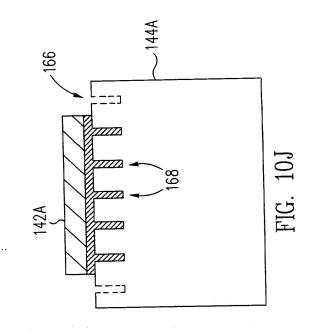


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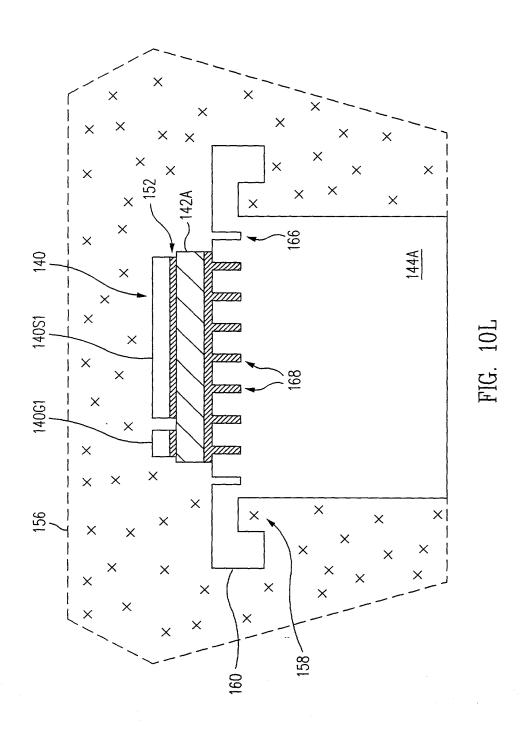






Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.

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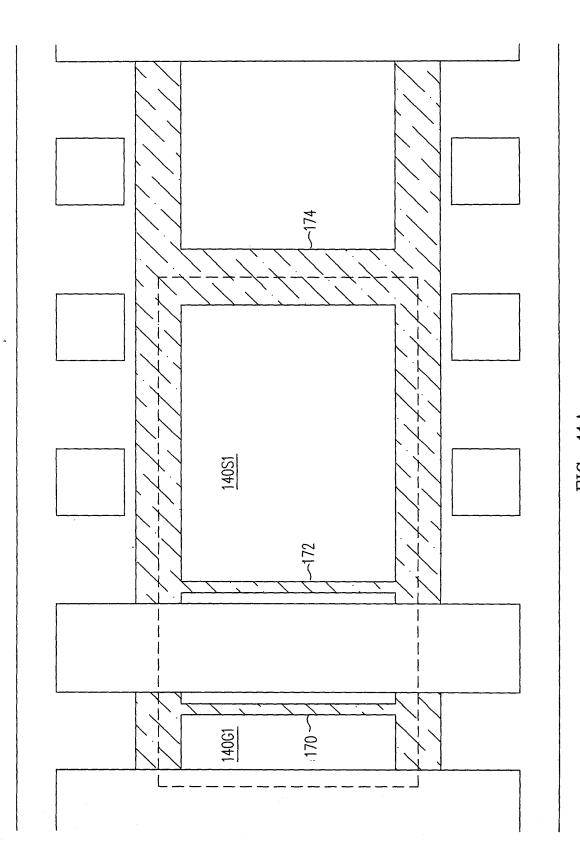
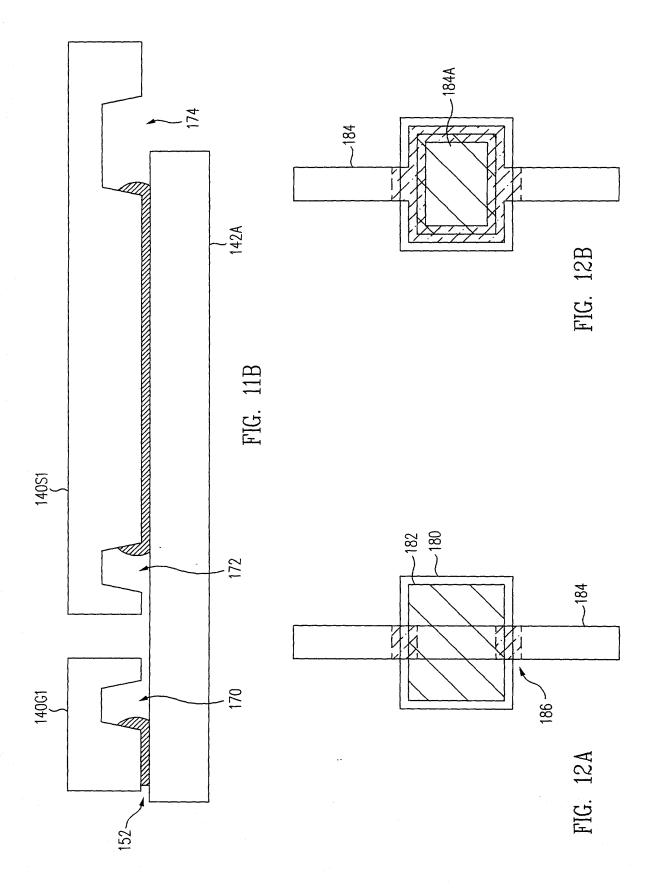
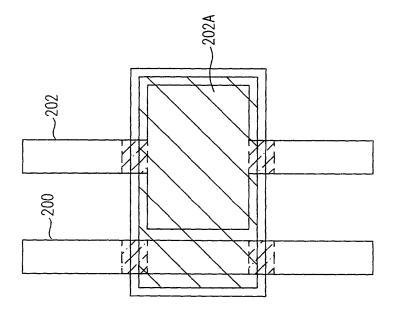


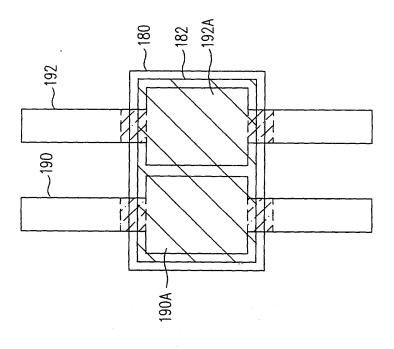
FIG. 11A



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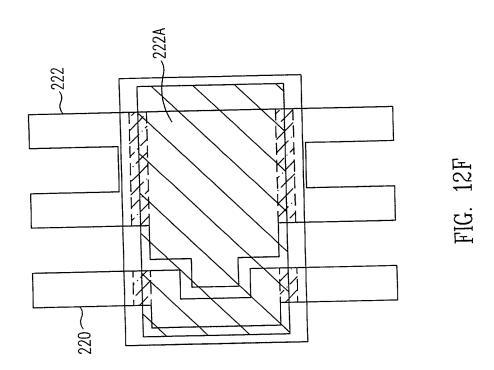


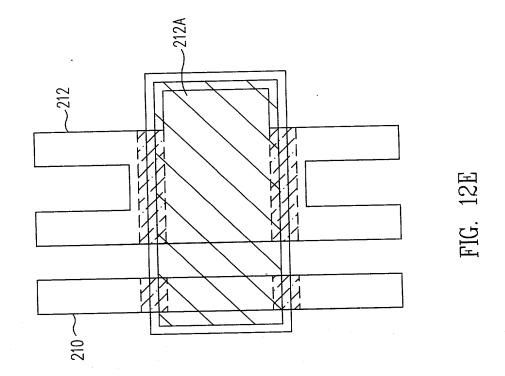




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Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 54 / 60





Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL.



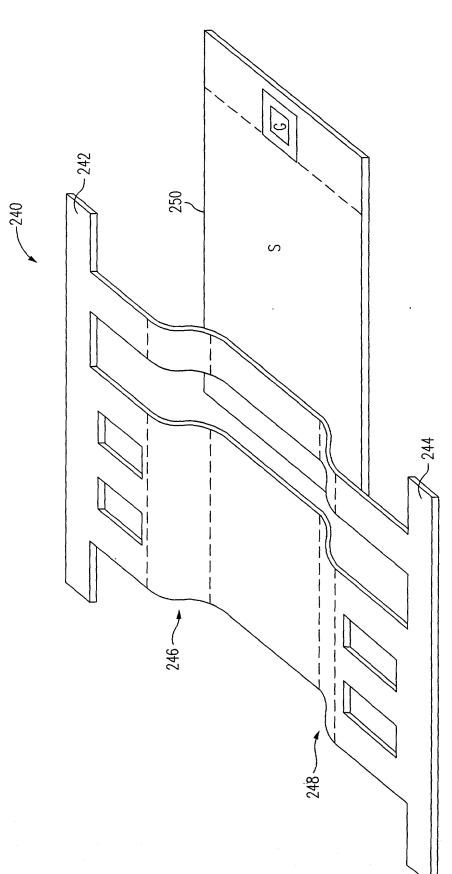


FIG. 13/

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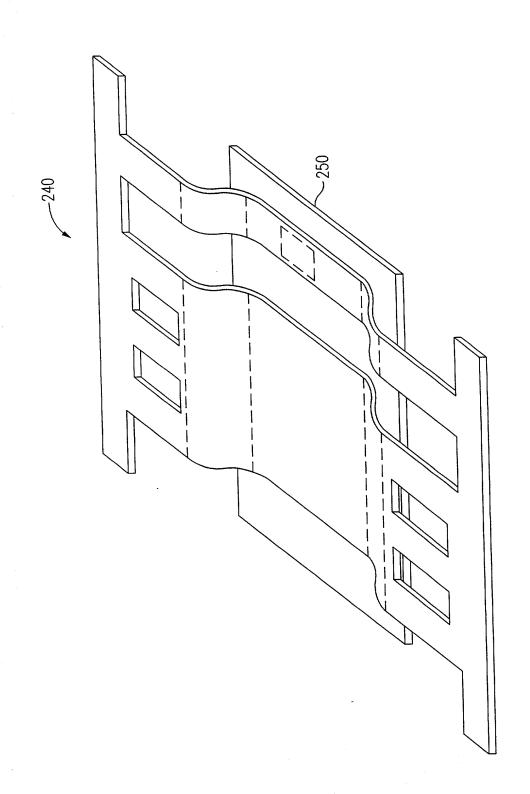
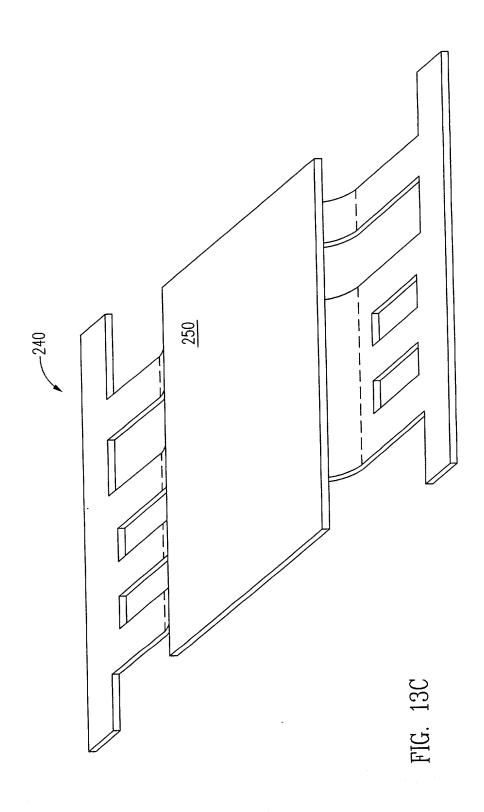
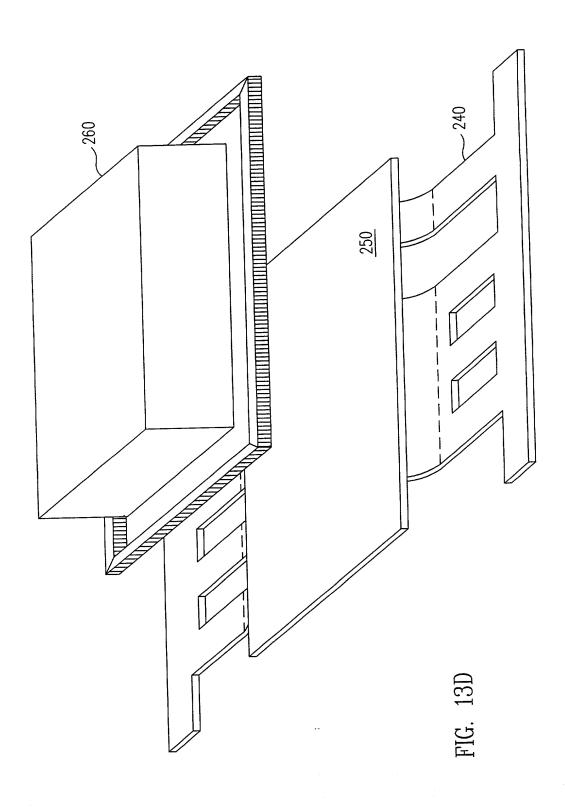


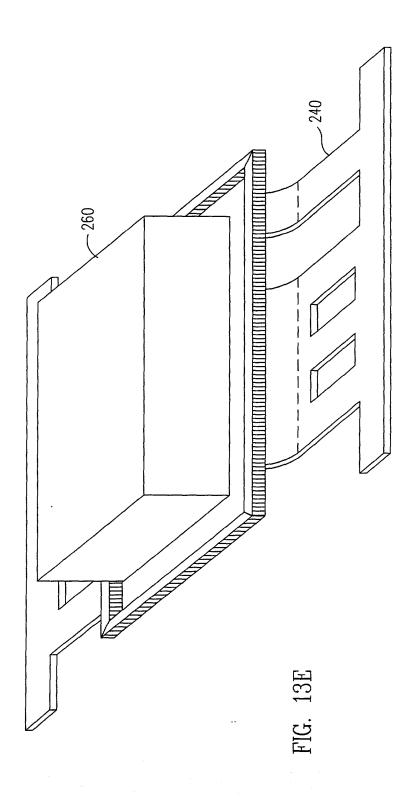
FIG. 13B



Symmetrical Package For Semiconductor Die Lam, Allen K. ET AL. 58/60



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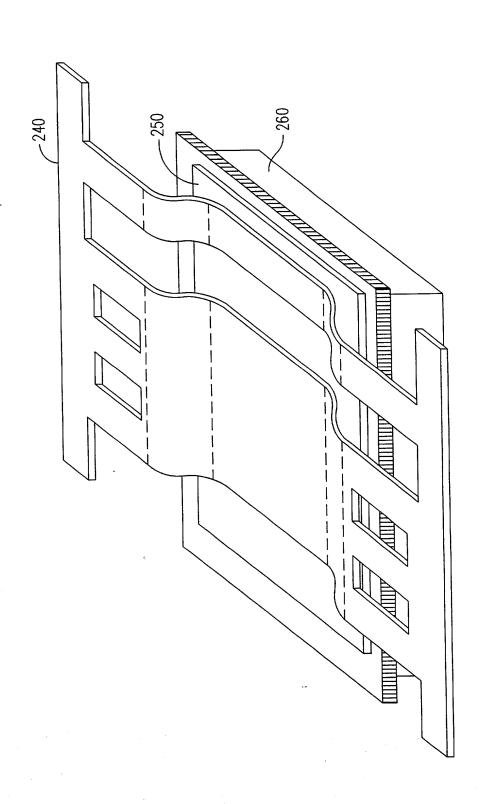


FIG. 13